

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

1300 I STREET, N. W.  
WASHINGTON, DC 20005-3315

202 • 408 • 4000  
FACSIMILE 202 • 408 • 4400

Jc531 U.S. PTO  
09/591510  
06/12/00

ATLANTA  
404•653•6400  
PALO ALTO  
650•849•6600

WRITER'S DIRECT DIAL NUMBER:

TOKYO  
011•813•3431•6943  
BRUSSELS  
011•322•646•0353

(202) 408-4039

June 12, 2000

ATTORNEY DOCKET NO. 07030.0017-00

**Box PATENT APPLICATION**  
**Assistant Commissioner for Patents**  
**Washington, D.C. 20231**

Re: New U.S. Patent Application  
Title: PROCESSOR FOR NETWORK COMMUNICATIONS  
WITH EFFICIENT USE OF PROCESSOR CYCLES  
Inventor: Robert Gelinis et al.

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application - 20 pages, including 6 independent claims and 26 claims total.
2. Drawings - 6 sheets of informal drawings (Figures 1 - 6).
3. A check for \$1,032.00 representing a \$690.00 filing fee and \$342.00 for additional claims.

This application is being filed under the provisions of 37 C.F.R. § 1.53(f). Applicants await notification from the Patent and Trademark Office of the time set for filing the Declaration.

Please accord this application a serial number and filing date.

06/12/00  
Jc803 U.S. PTO

09591510-061200

